Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	(first adj3 dummy adj3 cell) with (coupl$3 connect$3) with (second adj2 (bitline (bit adj line) digitline (digit adj line))) and (second adj3 dummy adj3 cell) with (coupl$3 connect$3) with (third adj2 (bitline (bit adj line) digitline (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 17:27
L2	7	(first adj3 dummy adj3 cell) with (coupl$3 connect$3) with (second adj2 (bitline (bit adj line) digitline (digit adj line))) same (second adj3 dummy adj3 cell) with (coupl$3 connect$3) with (third adj2 (bitline (bit adj line) digitline (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 17:28
L3	5	L1 and (ferroelectric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 17:29
L4	5	L2 and (ferroelectric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 17:29
S1	61	(first adj3 (sense adj2 (amp amplifier))) and (second adj3 (sense adj2 (amp amplifier))) and (fourth with (bit adj2 line)) and (dummy adj3 cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 12:54
S2	24	(first adj3 (sense adj2 (amp amplifier))) and (second adj3 (sense adj2 (amp amplifier))) and (fourth adj3 (bit adj2 line)) and (dummy adj3 cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 13:01
S3	9	(first adj3 (sense adj2 (amp amplifier))) and (second adj3 (sense adj2 (amp amplifier))) and (fourth adj3 (bit adj2 line)) and (second adj3 (dummy adj3 cell))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 13:03
S4	71	memory and (sense adj2 (amp amplifier)) and voltage and ((ferroelectric adj3 capacitor) same polari$6) and (bit adj2 line) and (dummy adj2 cell) and compar$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 13:36

S5	10	(memory and (sense adj2 (amp amplifier)) and voltage and ((ferroelectric adj3 capacitor) same polari$6) and (bit adj2 line) and (dummy adj2 cell) and compar$4) and (1T/1C)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 13:36
S6	7	((memory and (sense adj2 (amp amplifier)) and voltage and ((ferroelectric adj3 capacitor) same polari$6) and (bit adj2 line) and (dummy adj2 cell) and compar$4) and (1T/1C)) and ((short-circuit$3) short$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 13:37
S7	50	(memory and (sense adj2 (amp amplifier)) and voltage and ((ferroelectric adj3 capacitor) same polari$6) and (bit adj2 line) and (dummy adj2 cell) and compar$4) and ((short-circuit$3) short$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/28 13:51